



2121 #6
2/06/03

7217/65460

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Shigeo KUSUNOKI
Serial No.: 09/963,257
Filed: September 26, 2001
For: DISTORTION COMPENSATION APPARATUS AND
DISTORTION COMPENSATION METHOD
Group A.U.: 2121

I hereby certify that this paper is being deposited
this date with the U.S. Postal Service in first class
mail addressed to: Assistant Commissioner for
Patents, Washington, D.C. 20231.

Jay H. Maioli
Reg. No. 27,213

January 3, 2003

Date

RECEIVED

JAN 09 2003

Technology Center 2100

January 3, 2003
1185 Avenue of the Americas
New York, NY 10036
(212) 278-0400

030-
2FC1 / Repository Central

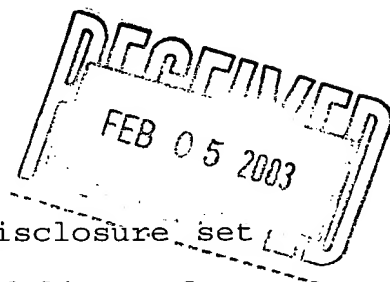
INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

As a means of complying with the duty of disclosure set forth in 37 CFR § 1.56 and in keeping with the guidelines of 37 CFR § 1.98, Applicant hereby submits information thought to be relevant to the examination of the above-identified application. Also submitted herewith is a completed form PTO-1449.

This information came to light during the examination of a counterpart application in the Singapore Patent Office in an Office Action dated October 15, 2002. Accordingly, the undersigned hereby certifies that the information submitted



herewith is being submitted within three months from the date of that Office Action.

Applicants, through his undersigned attorney, hereby certifies that, unless submitted herewith, no English language translation is presently available to those individuals identified in 37 CFR § 1.56(c) for any non-English language reference(s) cited.

To the best of the undersigned's knowledge, no Office Action has yet been received in the above-identified application.

U.S. Patent No. 5,781,069 (Baskin) apparently relates to a pre-post distortion amplifier.


U.S. Patent No. 5,770,971 (McNicol) apparently relates to distortion compensation control for a power amplifier.

Japanese Patent Abstract Publication No. 10270951 (Shimada et al.) apparently relates to distortion compensated amplifier.

Japanese Patent Abstract Publication No. 08125554 (Ishiguro) apparently relates to an automatic distortion compensation circuit.

No fee is deemed necessary in connection with the filing of this Information Disclosure Statement. However, if a fee is required for this submission, the Commissioner is authorized to charge the requisite fee to our Deposit Account No. 03-3125.

Respectfully submitted,
COOPER & DUNHAM LLP


Jay H. Maioli
Reg. No. 27,213

JHM/HYL
Enclosure

G:\Users\LIYHOLL\IDS\MAIOLI\Sony7217\65460\PTOLetter.wpd

Form PTO-1449

U.S. Department of Commerce
Patent and Trademark OfficeAtty. Docket No.
7217/65460Serial No.
09/963,257Applicant
Shigeo KusunokiFiling Date
September 26, 2001

Group

LIST OF PRIOR ART CITED BY APPLICANT
(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number							Date	Name	Class	Subclass	Filing Date if Appropriate
	AA	5	7	8	1	0	6	9	July 14, 1998	Baskin	H03F	1/26	
	AB	5	7	7	0	9	7	1	June 23, 1998	McNicol	H03F	3/66	
	AC												
	AD												

FOREIGN PATENT DOCUMENTS

		Document Number							Date	Country	Class	Subclass	Translation	
													Yes	No
	AE	10	2	7	0	9	5	1	September 10, 1998	Japanese Patent	H03F	1/32		
	AF	08	1	2	5	5	5	4	May 17, 1996	Japanese Patent	H04B	1/10		
	AG													
	AH													
	AI													

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AL	
	AM	
	AN	
	AO	
	AP	
	AQ	

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this from with next communication to applicant.